

Verilog® register transfer level synthesis

ICS 25.040

National foreword

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Summary of pages

This document comprises a front cover, an inside front cover, the IEC title page, pages 2 to 109 and a back cover.

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Amendments issued since publication

Amd. No.	Date	Comments

This British Standard was published under the authority of the Standards Policy and Strategy Committee on 5 December 2005

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