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13213

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Std 1212

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**Information technology –  
Microprocessor systems – Control  
and Status Registers (CSR) Architecture  
for microcomputer buses**

*Technologies de l'information –  
Systèmes à microprocesseurs – Architecture  
des registres de commande et d'état  
pour bus de micro-ordinateur*



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**Abstract:** The document structure and notation are described, and the objectives and scope of the CSR Architecture are outlined. Transition set requirements, node addressing, node architectures, unit architectures, and CSR definitions are set forth. The ROM specification and bus standard requirements are covered.

**Keywords:** CSR Architecture, bus architecture, bus standard, interoperability, microprocessors, node addressing, registers, transaction sets

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