

JESSI 0.8 μ m CMOS transistor model for analogue and digital circuit simulation

ICS 31.200

Committees responsible for this Published Document

The preparation of this Published Document was entrusted to Technical Committee GEL/93, Design automation, upon which the following bodies were represented:

- British Aerospace plc
- Department of Trade and Industry
- Federation of the Electronics Industry
- Institution of Electrical Engineers
- Manchester Metropolitan University
- University of Manchester
- University of Southampton

This Published Document, having been prepared under the direction of the Electrotechnical Sector Board, was published under the authority of the Standards Board and comes into effect on
15 September 1996

© BSI 1996

Amendments issued since publication

Amd. No.	Date	Text affected

The following BSI references relate to the work on this Published Document:
Committee reference GEL/93
Draft for comment 95/211311 SA

ISBN 0 580 26314 2
