



BSI Standards Publication

Test methods for electrical materials, printed boards and other interconnection structures and assemblies

Part 5-506: General test methods for materials and assemblies
— An intercomparison evaluation to implement the use of fine-pitch test structures for surface insulation resistance (SIR) testing of solder fluxes in accordance with IEC 61189-5-501

National foreword

This Published Document is the UK implementation of IEC TR 61189-5-506:2019.

The UK participation in its preparation was entrusted to Technical Committee EPL/501, Electronic Assembly Technology.

A list of organizations represented on this committee can be obtained on request to its secretary.

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